


Name/中文姓名	Wang Suzhen/王素珍	Gender	female	
Title (Pro./Dr.)	Professor, doctor of engineering	Country	China	
University/Department	College of Electronic & Information Engineering, Qingdao University, Qingdao 266071			
Personal Web Sites	https://www.qdu.edu.cn/			
Research Area	FPGA/SOPC application in communication system, signal detection and integrated circuit design			
<p>Brief introduction of your research experience:</p> <p>Wang Suzhen, female, doctor, professor, master tutor. Graduated from physics department of Northeast Normal University. At present, She is a teacher of the department of electronics and director of SOPC applied technology research center of Qingdao university. She has published many papers and presided over many projects. She has published many papers and compiled textbook "communication principle" which was published by Beijing posts and telecommunications in 2010. In recent years, She developed specific physical layer projects involved communication system using FPGA, such as the design of 2PSK, QPSK, QAM, DSSS spread spectrum and other communication systems based on FPGA devices, carrier synchronization, bit synchronization method extraction and other research. In the past two years, She has done some projects for Hisense in the following projects:</p> <p>LVDS low-voltage differential signal to TMDS format research based FPGA, Convert LVDS and mini-lvds signals for multi-channel and multi-format video transmission into standard TDMS signals.</p> <p>Development of video signal source, this video signal source can output video signals with resolution of 800*600, 1080*1920, 2160*3840, 1024*768 and so on.</p> <p>In a word, he applied FPGA to realize the communication system of physical layer in many applications.</p>				